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| SEARCHED | | | |
|---------------------|-------------------------------------|------------|-------|
| Class | Sub. | Date | Exmr. |
| 710 | 104 107 113 60 62 64 | | |
| 709 | 100 104 1 | | |
| 712 | 21 234 | 6. 17. 03 | X |
| update search above | | | |
| 712 | 28 | 12. 20. 03 | X |
| update search down | | | |
| 718 | 103 | 12- 2- 09 | X |

| SEARCH NOTES (INCLUDING SEARCH STRATEGY) | | |
|--|-----------|-------|
| | Date | Exmr. |
| Inventorship Search 09/838581 09/715778 | | |
| Web Search Thread Multiple thread SCSI Instruction buffer memory switch Thread register | | |
| East Search SCSI, program memory Fetch instruction Multiple thread Sequential processing thread / peripheral processing slice program counter Thread / wait, pause, stop | 6.17.03 | X |
| NPL Text book search | 6.17.03 | X |
| East Search Fork Multiple thread Simultaneous | 12.20.03 | X |
| East Search Supercell Super pipeline Flower Petals on record Inventorship Search update 4.814,978 6. 195.699 | | |
| NPL search IEEE, parallelism multiple thread | 12. 2. 04 | X |
| Consult with Richard | 12. 8. 04 | X |

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